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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10043002	FILING DATE 01/09/2002	CLASS 029	SUBCLASS	GAU 3729	EXAMINER [Signature]
**APPLICANTS: Canova Antonio;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/374,971 08/16/1999					
** FOREIGN APPLICATIONS VERIFIED: WORLD INTELLECTUAL PROPERTY ORGANI 98/36625 02/16/1998 ITALY FI97A000023 02/17/1997 PCT/IT98/00026 08/16/1999					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO N8125 DBP	
TITLE : Process for producing printed circuits and printed circuits thus obtained					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
		DRAWING	
		Sheets Drwg. Figs.Drwg. Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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